



PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Nobuaki HASHIMOTO

Application No.: 09/856,627

Filed: May 24, 2001

For: INTERCONNECT SUBSTRATE, SEMICONDUCTOR DEVICE, METHOD OF FABRICATING, INSPECTING AND MOUNTING THE SEMICONDUCTOR DEVICE, CIRCUIT BOARD, AND ELECTRONIC INSTRUMENT

Group Art Unit: 2822

Examiner: I. SOWARD

Docket No.: 109609

#18/RCE
A700d
3/25/03

**LARGE ENTITY REQUEST FOR
CONTINUED EXAMINATION UNDER 37 C.F.R. §1.114**

Director of the U.S. Patent and Trademark Office
Washington, D.C. 20231

Sir:

In accordance with the provisions of 37 C.F.R. §1.114, Applicant hereby requests continued examination and entry and consideration of the February 19, 2003 submission. The above-identified application was filed on or after June 8, 1995. Thus, entry is proper under 37 C.F.R. §1.114(d).

Attached hereto is our check no. 140272 in the amount of \$750.00 as payment of the fees set forth in 37 C.F.R. §1.17(e). The Director is hereby authorized to charge any additional fees or credit any overpayment associated with this communication to Deposit Account No. 15-0461. Two duplicate copies of this page are enclosed.

Respectfully submitted,

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750.00 OP

JAO:MB/jfl

Date: March 18, 2003

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